

Electronic Acknowledgement Receipt

EFS ID:	1532107
Application Number:	10572730
International Application Number:	
Confirmation Number:	6903
Title of Invention:	Semiconductor device having high-k gate dielectric layer and method for manufacturing the same
First Named Inventor/Applicant Name:	Hiroshi Oji
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Attorney Docket Number:	40404.30/tt
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	40404-30-PTO-PCT.pdf	216039	no	5

Warnings:

Information:					
2	Information Disclosure Statement (IDS) Filed	40404-30-PTO-20070221-US_IDS_Form__SB_08a.pdf	657186	no	4

Warnings:

Information:

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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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